

YOR920010565US2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re divisional patent application of 10/224,899 filed on August 21, 2002

Clevenger et al.

Serial No.: Not Yet Assigned

Group Art Unit: Unknown

Filing Date: Concurrently Herewith

Examiner: Unknown

For: INTEGRATED METAL-INSULATOR-METAL CAPACITOR METAL  
GATE TRANSISTOR

Commissioner of Patents  
PO BOX 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Sir:

Prior to examination on the merits and calculation of the filing fee, please amend the  
above-identified application as follows:

**IN THE SPECIFICATION:**

Page 1, after line 3, insert the following:

-- Cross-Reference To Related Applications

This application is a division of U.S. Application Serial Number 10/224,899 filed  
August 21, 2002. --

**IN THE DRAWINGS:**

Please substitute the attached formal drawings (5 sheets, containing Figs. 1-13) for the  
drawings which were originally filed with the parent application. The drawings originally